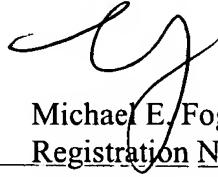


16-18 rewritten so as to eliminate the multiple dependency. Attached hereto is a marked-up version of the claims as amended. Entry of this preliminary amendment is respectfully requested.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



Michael E. Fogarty
Registration No. 36,139

600 13th Street, N.W.
Washington, DC 20005-3096
(202) 756-8000 MEF:prp
Date: March 1, 2002
Facsimile: (202) 756-8087

201002-00053001

MARKED-UP VERSION OF AMENDED CLAIMS

The Claims have been amended as follows:

16. The semiconductor integrated circuit of Claim 13, [any of Claims 13, 14 and 15]
wherein the width of the plurality of interconnection lines is 0.18 μm or less.

17. The semiconductor integrated circuit of Claim 13, [any of Claims 13, 14 and 15]
wherein the plurality of interconnection lines are a plurality of address bus lines.

18. The semiconductor integrated circuit of Claim 13, [any of Claims 13, 14 and 15]
wherein signals propagating through the plurality of interconnection lines are digital signals of an
image or voice.

The following claims have been added:

35. The semiconductor integrated circuit of Claim 14, wherein the width of the plurality
of interconnection lines is 0.18 μm or less.

36. The semiconductor integrated circuit of Claim 15, wherein the width of the plurality
of interconnection lines is 0.18 μm or less.

37. The semiconductor integrated circuit of Claim 14, wherein the plurality of
interconnection lines are a plurality of address bus lines.

38. The semiconductor integrated circuit of Claim 15, wherein the plurality of

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[illegible]